

**ARM<sup>®</sup> Cortex<sup>®</sup>-M  
32-bit Microcontroller**

**NuMicro<sup>®</sup> Family  
NUC122 Series BSP  
Revision History**

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**Revision 3.00.005** (Released 2021-01-20)

1. Modified to pass USB-IF CV-Chapter 9 & Class test of all USB D Sample code.
2. Added Apache-2.0 license declaration in driver source.
3. Added README.md file.

**Revision 3.00.004** (Released 2019-11-11)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Fixed PWM\_DisableCaptureInt of PWM driver.
4. Fixed CLK\_SetHCLK() bug of CLK driver.
5. Fixed CLK\_EnablePLL() wrong PLL default setting value of CLK driver.

**Revision 3.00.003** (Released 2017-10-24)

1. Fixed PLL clock source selection bug in CLK\_SetCoreClock().
2. Fixed clear Receive Line Status interrupt flag bug in UART\_ClearIntFlag().
3. Disable debug message when enabling semihost without NuLink connection.
4. Added CLK\_SysTickLongDelay() for long delay.

**Revision 3.00.002** (Released 2015-07-24)

1. Fix the the reset vector handler to Reset\_Handler of all sample codes.
2. Fix maximum USB endpoint from 8 to 6 in USB driver, because of NUC122 USB D Endpoint number is 6 only.
3. Fix UART transmit data bug in UART\_TEST\_HANDLE() of UART\_TxRx\_Function sample code.
4. Fix FMC\_Erase() ISPF flag clear to avoid ISP disable when error in FMC driver.
5. Remove APUEN enable or disable macro in FMC driver. NUC122AN doesn't support this function.
6. Remove ISPATA, VECMAP, UID, UCID from FMC driver, because they are not supported in NUC122.
7. Revise the following four macro definitions in SPI driver to avoid affecting another SPI\_SS pin. SPI\_SET\_SS0\_HIGH() SPI\_SET\_SS1\_HIGH() SPI\_SET\_SS0\_LOW() SPI\_SET\_SS1\_LOW()
8. Update USB D driver for better compatibility
9. Add USB D Billboard sample code to show the implementation of Billboard class.
10. Add Hard\_Fault\_Sample to show how to implement hard fault handler.
11. Add non-block printf supporting in retarget.c
12. Add UART FIFO size constants definitions with UART driver.
13. Add new function to control systick and select systick clock source, CLK\_EnableSysTick() and CLK\_DisableSysTick() in CLK driver.
14. Add UART\_Wakeup sample code to demonstrate how to wakeup system by UART

**Revision 3.00.001** (Released 2015-01-15)

1. Update to support new driver API

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