

**ARM® Cortex®-M4**  
**32-bit Microcontroller**

**M480 Series CMSIS BSP**  
**Revision History**

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### Revision 3.05.005 (Released 2023-03-16)

1. Added SampleCode\PowerManagement folder for power down modes
2. Added ID3v1 & ID3v2 parser to I2S\_MP3PLAYER sample code
3. Added sample code SYS\_PowerDown\_MinCurrent.
4. Updated retarget.c to fix compile errors on IAR version 8.0 or later.

### Revision 3.05.004 (Released 2022-12-20)

1. Fixed an AES driver bug in MbedTLS porting.
2. Updated smart card library to support cards not compatible with ISO-7816.
3. Fixed SPIM sample code 4-bytes address mode issue.
4. Fixed code size issue of the SecureBoot firmware update samples.
5. Added UART6/UART7 setting to UART driver for M480LD series.
6. Minor bug fix.

### Revision 3.05.003 (Released 2022-03-03)

1. Remove emWin samples and library.
2. Adjust flash and SRAM size of USB samples to fit M483.
3. Add RTC GPIO pin control in DPD mode to fix current leakage issue.
4. Fix USBH EHCI driver isochronous interval bug.

### Revision 3.05.002 (Released 2021-12-28)

1. Add time-out check to infinite loop in drivers and samples.
2. Enable I2C and USCI\_I2C pin schmitt trigger.
3. Modify HSUSB HID samples to pass USBIF.
4. Add remote wakeup to HID mouse sample.
5. Minor bug fix.

### Revision 3.05.001 (Released 2020-10-8)

1. Added Apache-2.0 license declaration into driver source code.
2. Added APROM\_Loader, CCAP\_Packet\_JpegEncode, I2C\_SMBus, and ISP\_UART\_SPIFLASH\_M487MK sample codes.
3. Added EPWM\_EnableADCTriggerPrescale(), EPWM\_DisableADCTriggerPrescale(), EPWM\_EnableFaultDetect(), EPWM\_DisableFaultDetect(), EPWM\_EnableFaultDetectOutput(), EPWM\_DisableFaultDetectOutput(), EPWM\_EnableFaultDetectDeglitch(), EPWM\_DisableFaultDetectDeglitch(), EPWM\_EnableFaultDetectMask(), EPWM\_DisableFaultDetectMask(), EPWM\_EnableFaultDetectInt(), EPWM\_DisableFaultDetectInt(), EPWM\_ClearFaultDetectInt(), and EPWM\_GetFaultDetectInt() APIs.
4. Added USPI\_DISABLE\_TX\_RX\_PDMA() and USPI\_ENABLE\_TX\_RX\_PDMA(), QSPI\_TRIGGER\_TX\_RX\_PDMA, QSPI\_DISABLE\_TX\_RX\_PDMA(), SPI\_TRIGGER\_TX\_RX\_PDMA, SPI\_DISABLE\_TX\_RX\_PDMA(), UART\_TRIGGER\_RX\_PDMA(), UART\_TRIGGER\_TX\_PDMA(), UART\_DISABLE\_RX\_PDMA(), and UART\_DISABLE\_TX\_PDMA() macro definitions.
5. Minor bug fix.

### Revision 3.05.000 (Released 2020-3-6)

1. Added M487KMCAN support.

2. Added QSPI\_Slave3Wire, SPI\_HalfDuplex, USB\_D\_Mass\_Storage\_SD, HSUSB\_D\_Mass\_Storage\_SD, HSUSBH\_USBH\_HID\_Mouse\_Keyboard, HSUSB\_D\_Audio10\_Headset, HSUSB\_D\_Audio20\_Headset, ISP\_SPI, ISP\_DFU, and ISP\_DFU\_20 sample codes.
3. Added SPIM\_WinbondUnlock(), EMAC\_FillCamEntry(), EMAC\_SendPktWoCopy(), EMAC\_ClaimFreeTXBuf(), EMAC\_GetAvailRXBufSize(), and EMAC\_RecvPktDoneWoRxTrigger() APIs.
4. Added TIMER\_SET\_OPMODE(), UART\_PDMA\_ENABLE(), UART\_PDMA\_DISABLE(), UART\_PDMA\_ENABLE(), UART\_PDMA\_DISABLE(), EMAC\_ENABLE\_INT(), EMAC\_DISABLE\_INT(), EMAC\_GET\_INT\_FLAG(), and EMAC\_CLEAR\_INT\_FLAG() macro definitions.
5. Removed static qualifier of EMAC\_PhyInit() which must now be called after EMAC\_Open().
6. Updated UsbHostLib to improve USB 2.0 interrupt transfer performance.
7. Minor bug fix.

#### Revision 3.04.000 (Released 2019-7-5)

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1. Added M48xGC/M48xG8 support.
2. Reorganized sample directory for NuMaker-ETM-M487, NuMaker-PFM-M487, and NuMaker-PFM-M487D boards.
3. Minor bug fix.

#### Revision 3.03.001 (Released 2018-10-5)

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1. Added emWin quick start guide.
2. Fixed GCC compilation errors in the Linux environment.

#### Revision 3.03.000 (Released 2018-05-30)

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1. Added emWin sample codes and library.
2. Updated Eclipse project directory structure.
3. Updated audio sample MFP setting to match current NuMaker board design.
4. Minor bug fix.

#### Revision 3.02.000 (Released 2018-03-20)

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1. Updated sample codes to use PB12(RX) and PB13(TX) instead of PD2(RX) and PD3(TX) as console pins for NuMaker-PFM-M487 v3.0 board.
2. Updated FatFs to r0.13a.
3. Updated FreeRTOS to v10.0.0.
4. Updated lwIP to v2.0.3.
5. Added a USBH CDC class driver and a CDC class VCOM sample.
6. Added mbed TLS and test suites using crypto accelerator.
7. Minor bug fix.

#### Revision 3.01.000 (Released 2017-09-29)

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1. Reorganized register definition header files.
2. Added new standard driver reference samples.
3. Added Eclipse project support.
4. Renamed SPI0 to QSPI0, SPI1 to SPI0, SPI2 to SPI1, SPI3 to SPI2, SPI4 to SPI3, ACMP to ACMP01.
5. Rewrote USBH library to optimize memory usage.

6. Minor bug fix.

**Revision 3.00.000** (Released 2017-06-15)

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1. Initial release.

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