









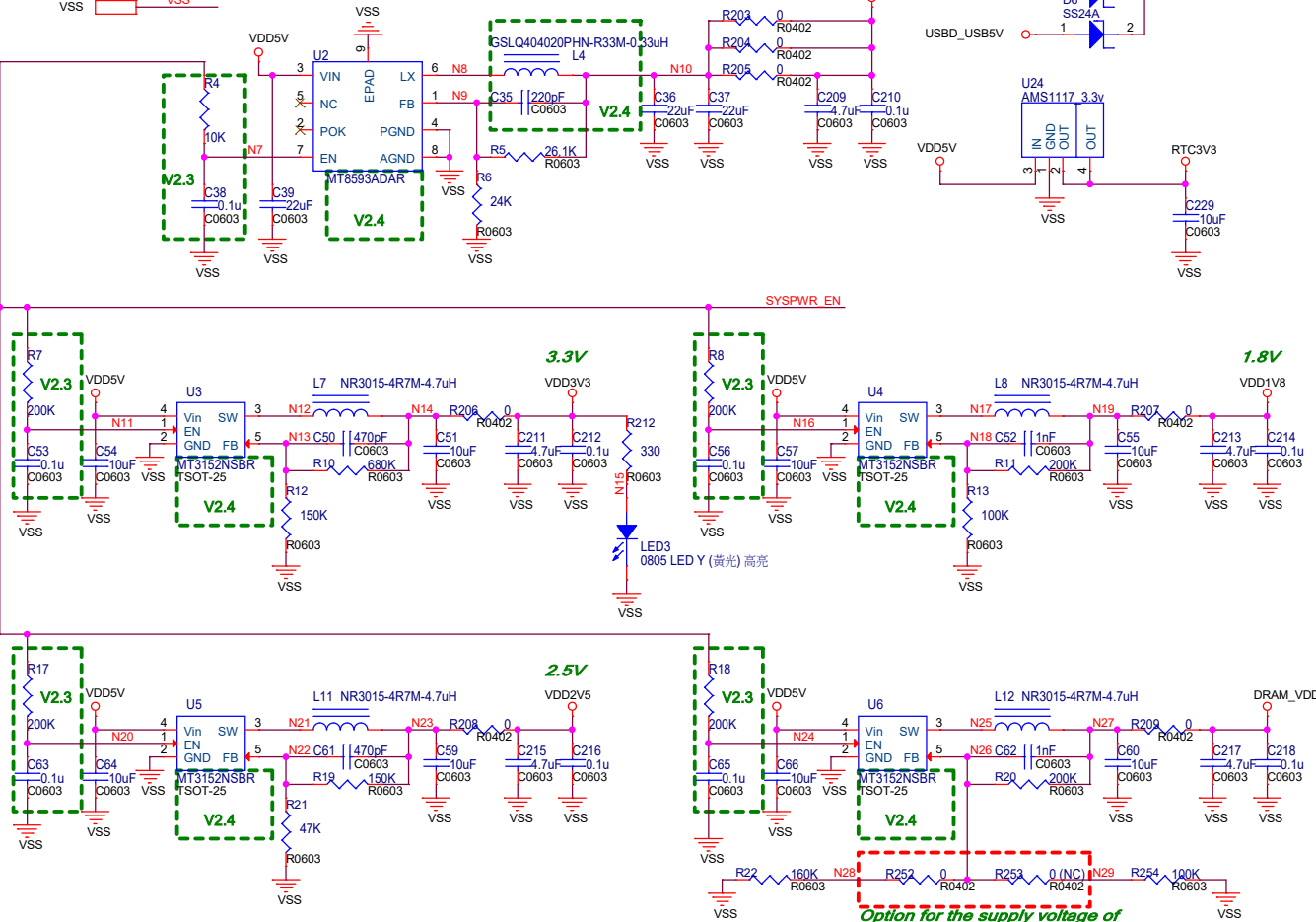


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Title			NuMaker-IoT-MA35D16F90 (LQFP216)	
Size	Document Number		Rev	
A	00. System Block		V2.4	
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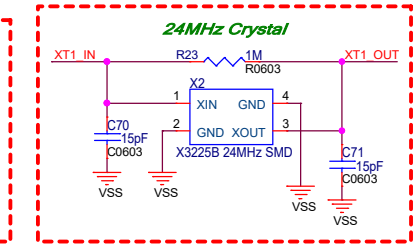
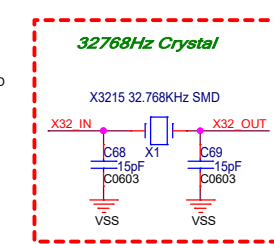
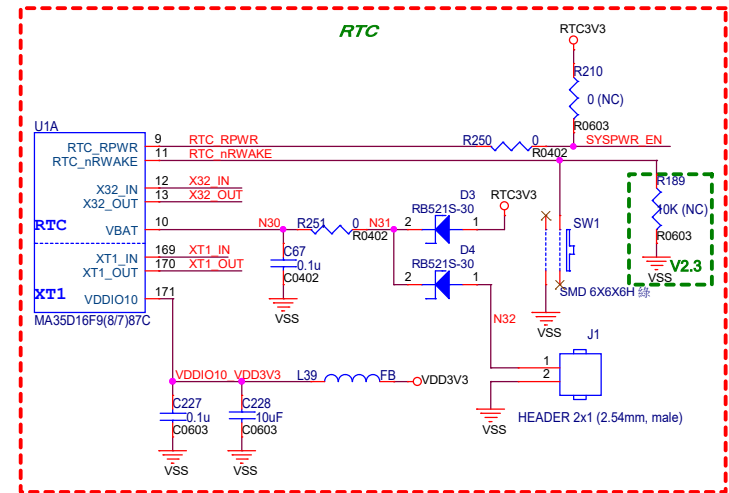
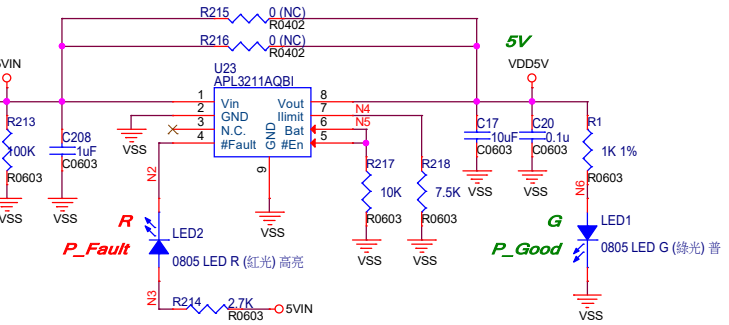
SYSPWR_EN  SYSPWR_EN
 N123_USB5V  N123_USB5V
 USBD_USB5V  USBD_USB5V
 VDD5V  VDD5V
 VDD3V3  VDD3V3
 VDD2V5  VDD2V5
 VDD1V8  VDD1V8
 DRAM_VDD  DRAM_VDD
 VDD1V2  VDD1V2
 VSS  VSS

Note: Please make sure the voltage of VDD_CORE power pins (fed from VDD1V2) of MA35D16F9(8/7)87C chip is above 1.20V, including the minimum value of ripple.



Option for the supply voltage of SDRAM power (DRAM_VDD)

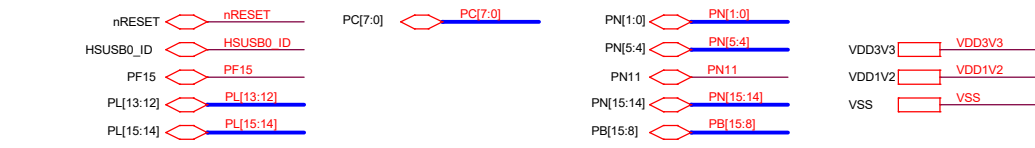
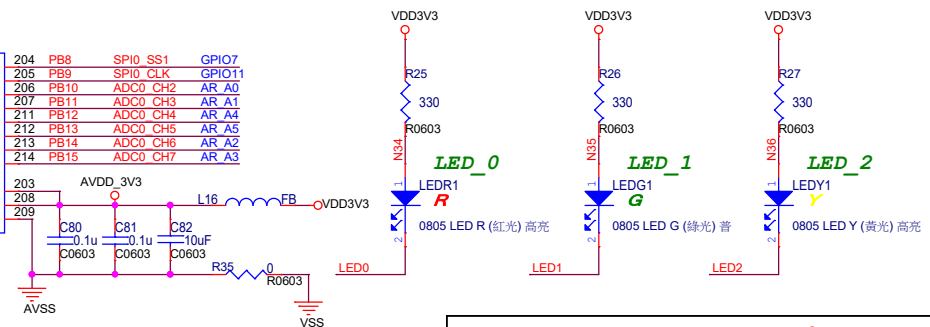
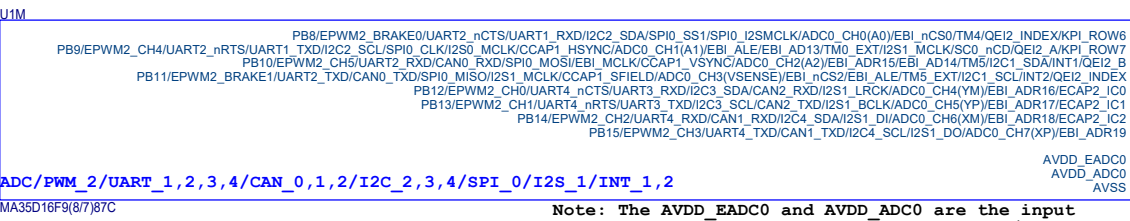
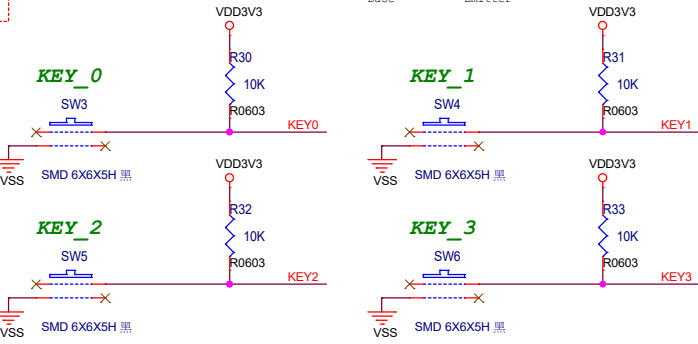
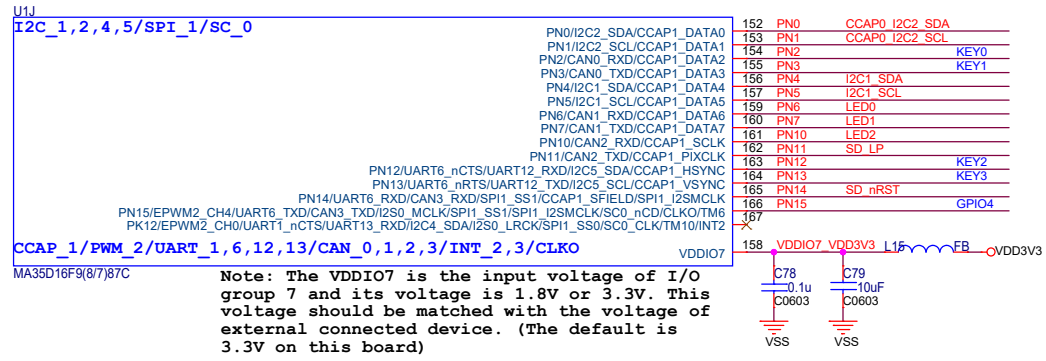
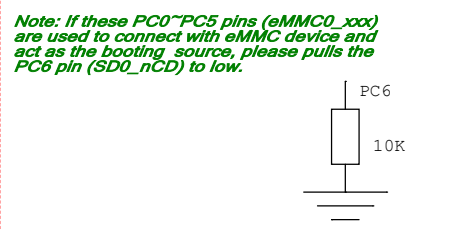
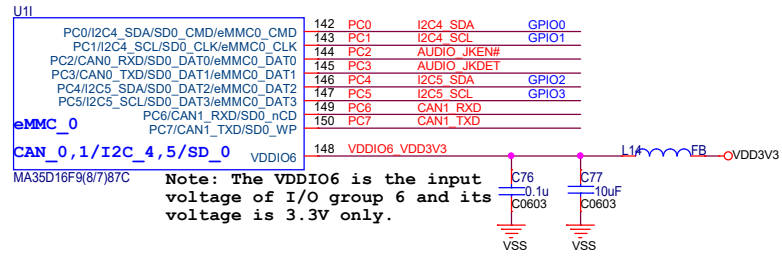
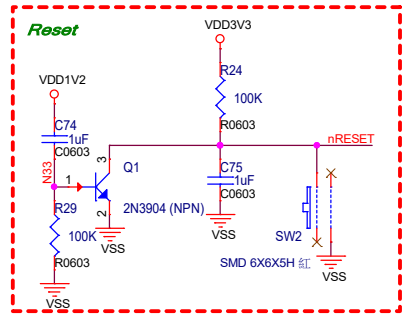
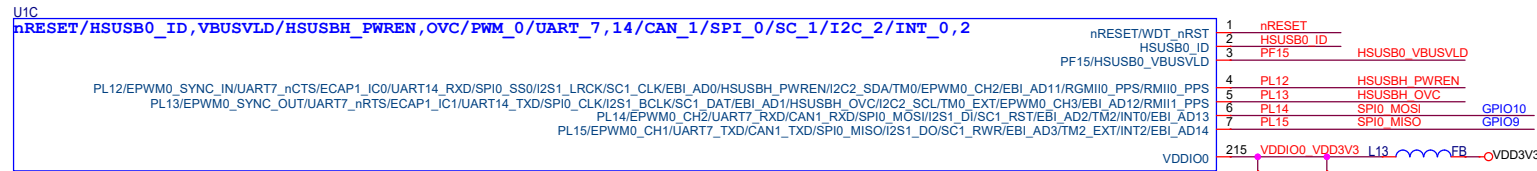
1. Mount R252 only, DRAM_VDD = 1.35V (for MA35D16F987C/MA35D16F887C, DDR3L)
2. Mount R253 only, DRAM_VDD = 1.8V (for MA35D16F787C, DDR2)



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Title

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02. VDDIO0/6/7/ADC

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U1E

PK9/I2C3_SCL/CCAP0_SCLK/EBI_AD0/EBI_ADR0
 PK10/CAN1_RXD/CCAP0_PIXCLK/EBI_AD1/EBI_ADR1
 PK11/CAN1_TXD/CCAP0_HSYNC/EBI_AD2/EBI_ADR2
 PM0/I2C4_SDA/CCAP0_VSYNC/EBI_AD3/EBI_ADR3
 PM1/I2C4_SCL/SP13_I2SMCLK/CCAP0_SFIDE/EBI_AD4/EBI_ADR4
 PM2/CAN3_RXD/CCAP0_DATA0/EBI_AD5/EBI_ADR5
 PM3/CAN3_TXD/CCAP0_DATA1/EBI_AD6/EBI_ADR6
 PM4/I2C5_SDA/CCAP0_DATA2/EBI_AD7/EBI_ADR7
 PM5/I2C5_SCL/CCAP0_DATA3/EBI_AD8/EBI_ADR8
 PM6/CAN0_RXD/CCAP0_DATA4/EBI_AD9/EBI_ADR9
 PM7/CAN0_TXD/CCAP0_DATA5/EBI_AD10/EBI_ADR10
 PM8/I2C0_SDA/CCAP0_DATA6/EBI_AD11/EBI_ADR11
 PM9/I2C0_SCL/CCAP0_DATA7/EBI_AD12/EBI_ADR12
 PM10/EPWM1_CH2/CAN2_RXD/SP13_SS0/CCAP0_DATA8/SP12_I2SMCLK/EBI_AD13/EBI_ADR13
 PM11/EPWM1_CH3/CAN2_TXD/SP13_SS1/CCAP0_DATA9/SP12_SS1/EBI_AD14/EBI_ADR14

CCAP_0/PWM_1/CAN_0,1,2,3/I2C_0,3,4,5

VDDIO2

MA35D16F9(8/7)87C

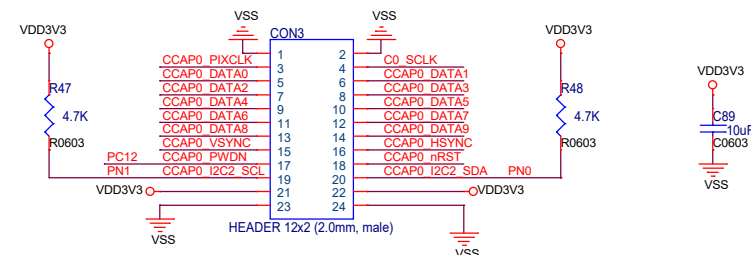
Note: The VDDIO2 is the input voltage of I/O group 2 and its voltage is 1.8V or 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 3.3V on this board)

65 PK9 CCAP0_SCLK R48 33 C0_SCLK
 66 PK10 CCAP0_PIXCLK R0603 C88
 67 PK11 CCAP0_HSYNC NC
 68 PM0 CCAP0_VSYNC C0603
 69 PM1 CCAP0_nRST
 70 PM2 CCAP0_DATA0
 71 PM3 CCAP0_DATA1
 72 PM4 CCAP0_DATA2
 73 PM5 CCAP0_DATA3
 74 PM6 CCAP0_DATA4
 75 PM7 CCAP0_DATA5
 76 PM8 CCAP0_DATA6
 77 PM9 CCAP0_DATA7
 78 PM10 CCAP0_DATA8
 79 PM11 CCAP0_DATA9

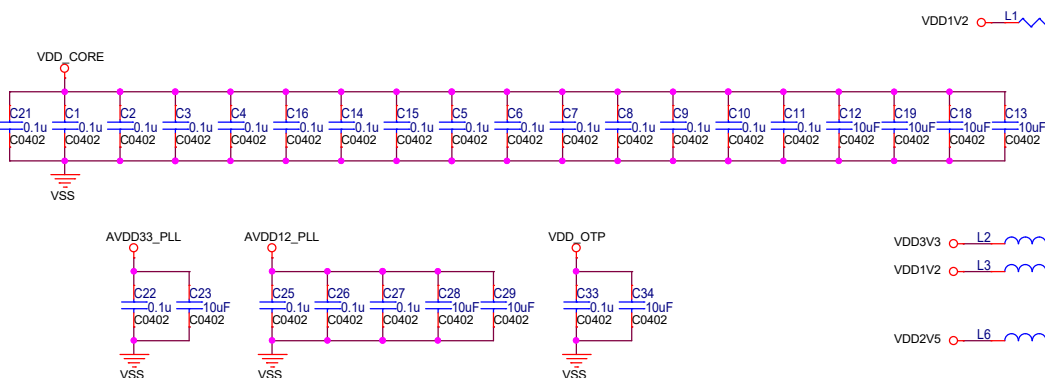
76 CCAP0_VDD3V3 L18 FB VDD3V3

C90 0.1uF C0603
 C91 10uF C0603

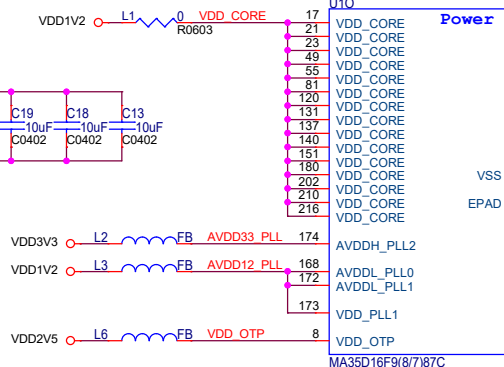
CCAP0 Connector



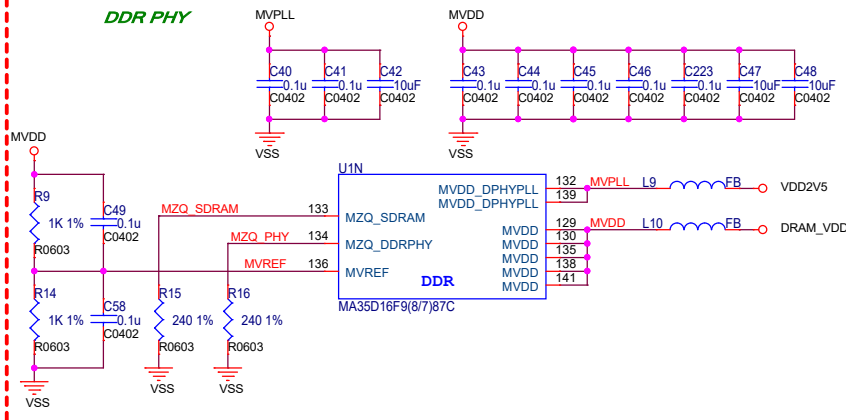
Power



Note: Please make sure the voltage of VDD_CORE power pins (fed from VDD1V2) of MA35D16F9(8/7)87C chip is above 1.20V, including the minimum value of ripple.



DDR PHY



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Title
NuMaker-IoT-MA35D16F90 (LQFP216)

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04. CCAP0 (VDDIO2)

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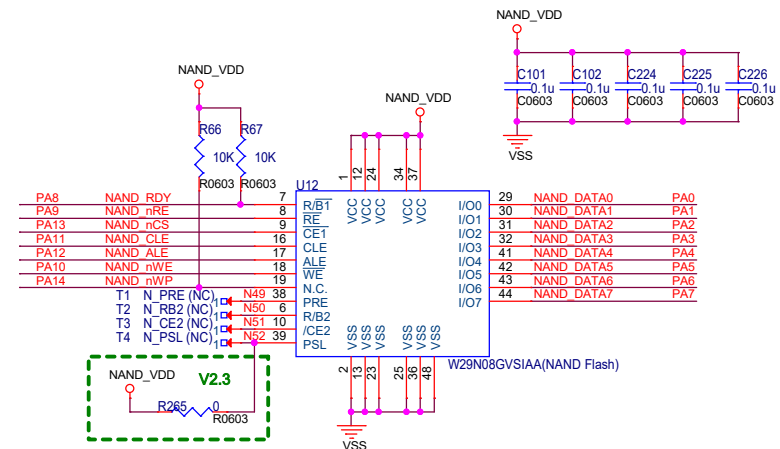
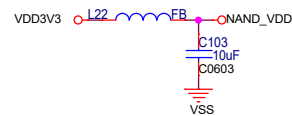
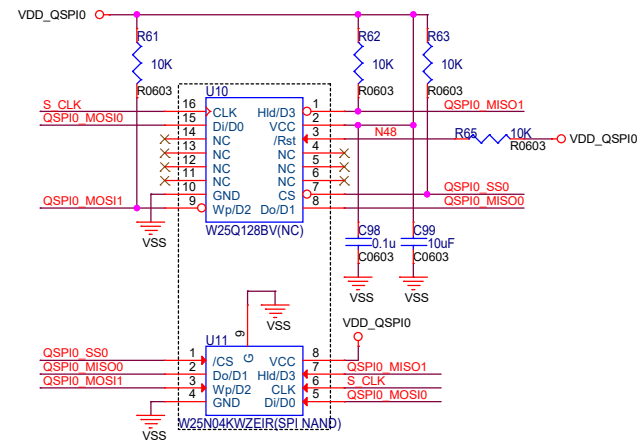
11H

PD0/UART3_nCTS/UART4_RXD/QSPI0_SS0	122	PD0	R255	33	R0402	QSPI0_SS0
PD1/UART3_nRTS/UART4_TXD/QSPI0_CLK	123	PD1				
PD2/UART3_RXD/QSPI0_MOSI0	124	PD2	R257	33	R0402	QSPI0_MOSI0
PD3/UART3_TXD/QSPI0_MISO0	125	PD3	R258	33	R0402	QSPI0_MISO0
PD4/UART1_nCTS/UART2_RXD/I2C2_SDA/QSPI0_MOSI1	126	PD4	R259	33	R0402	QSPI0_MOSI1
PD5/UART1_nRTS/UART2_TXD/I2C2_SCL/QSPI0_MISO1	127	PD5	R260	33	R0402	QSPI0_MISO1

QSPI0_U0/UART_2,3,4/I2C_2 VDDIO5

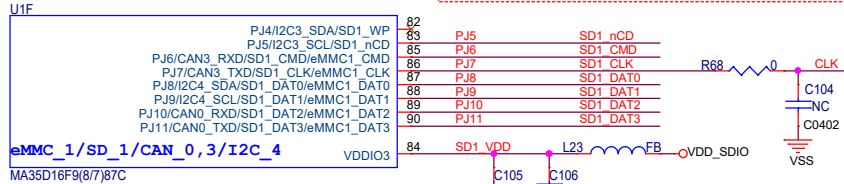
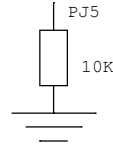
MA35D16F9(8/7)8C **Note: The VDDIO5 is the input voltage of** C97

Q/VDD_QSPI0

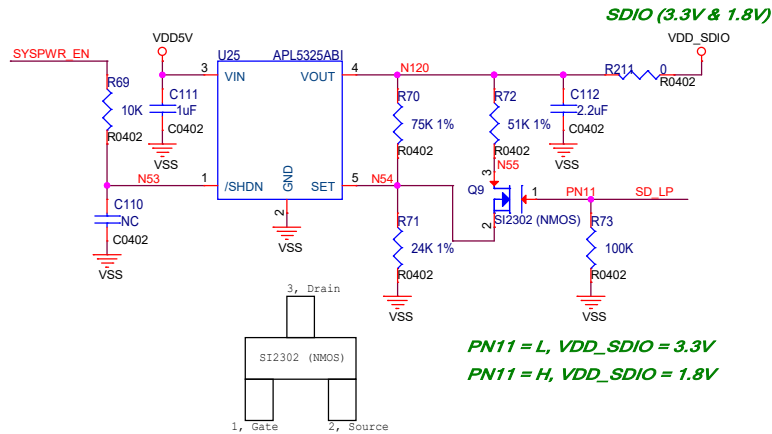


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Note: If these PJ6~PJ11 pins (eMMC1_xxx) are used to connect with eMMC device and act as the booting source, please pulls the PJ5 pin (SD1_nCD) to low.



Note: The VDDIO3 is the input voltage of I/O group 3 and its voltage is 1.8V or 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 3.3V but can be controlled by GPIO PN11 high or low state that follows the SD3.0 timing on this board)



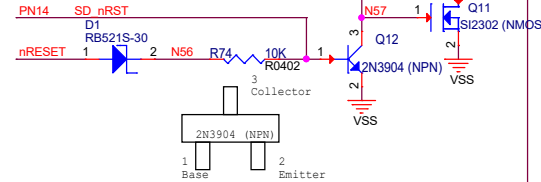
For SD card compatibility

PN14 = L, VDD_SD OFF

PN14 = H, VDD_SD ON

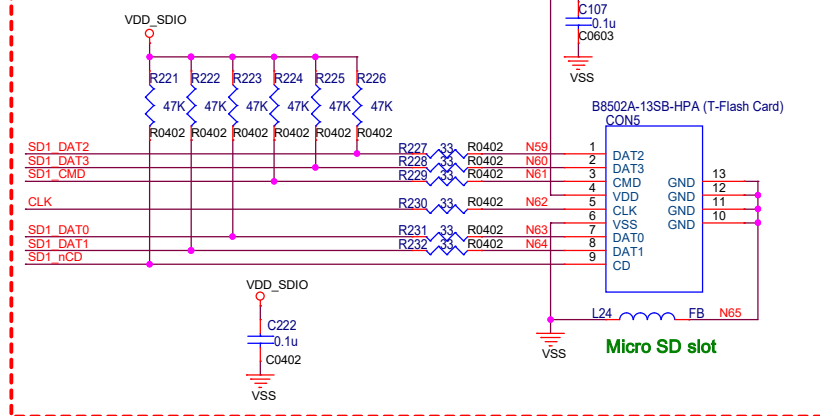
nRESET = L, VDD_SD OFF

nRESET = H, VDD_SD ON



SD1

VDD_SDIO

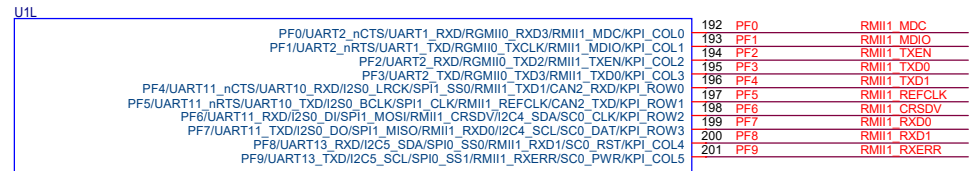


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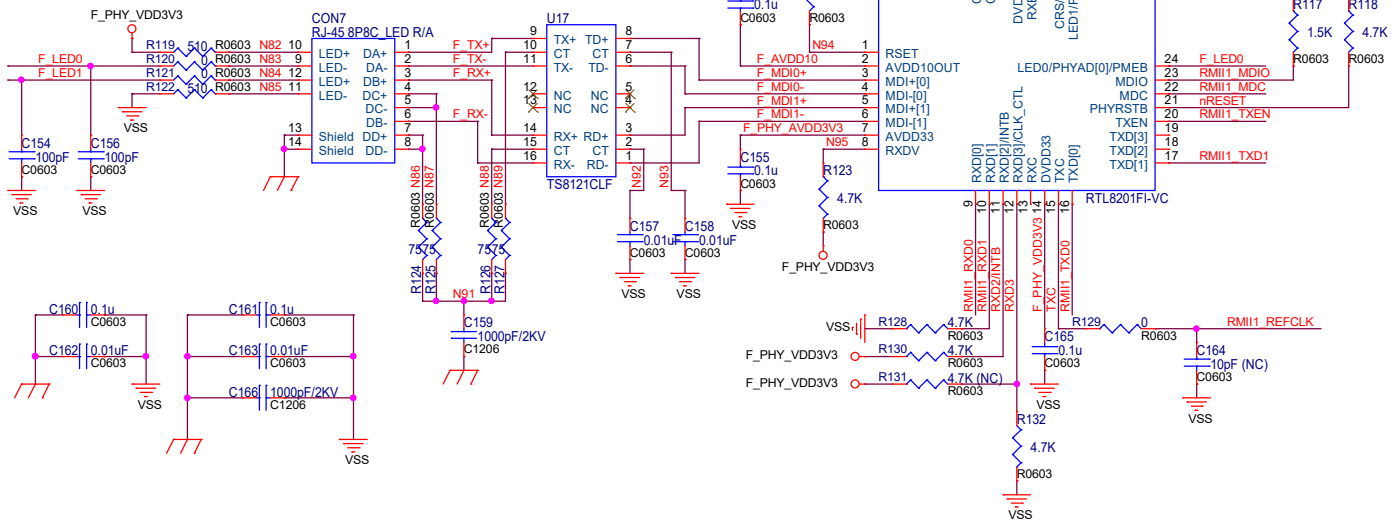
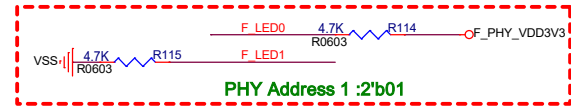
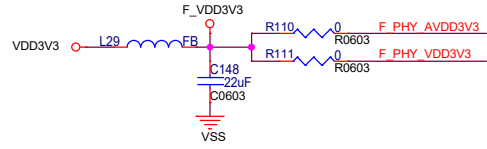
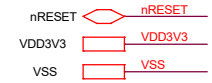
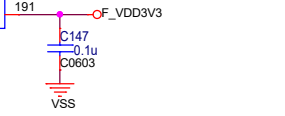
Size B Document Number
07. SD1 (VDDIO3)

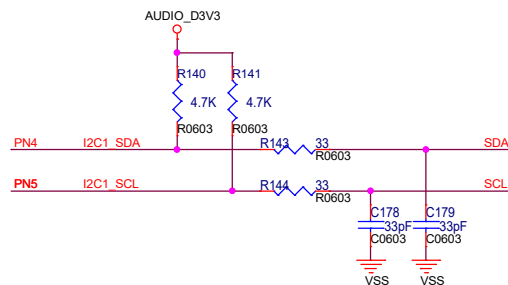
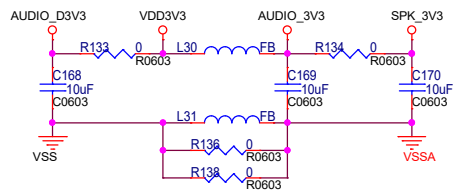
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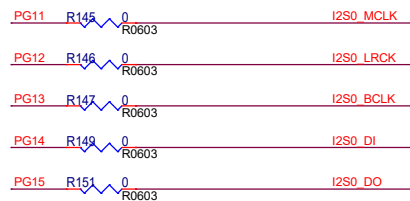
MA35D16F9(8/7)87C

Note: The VDDIO9 is the input voltage of I/O group 9 and its voltage is 1.8V or 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 3.3V on this board)

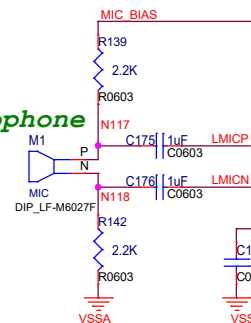




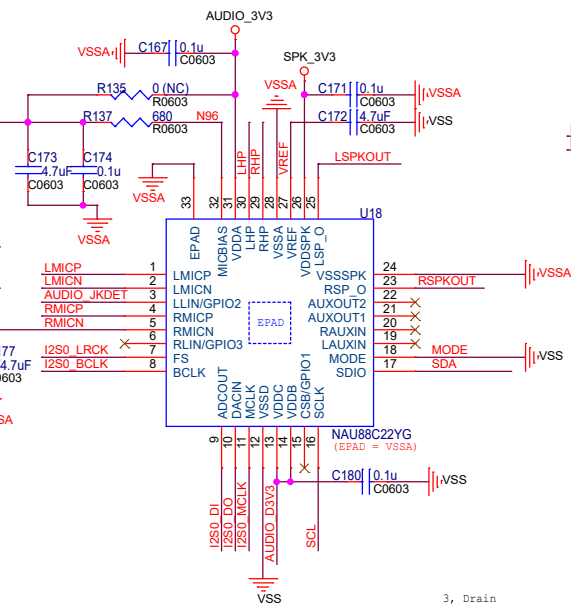
PG11~15 Connect to I2S0 (SWJ)



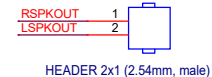
Microphone



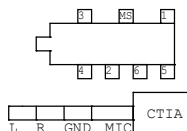
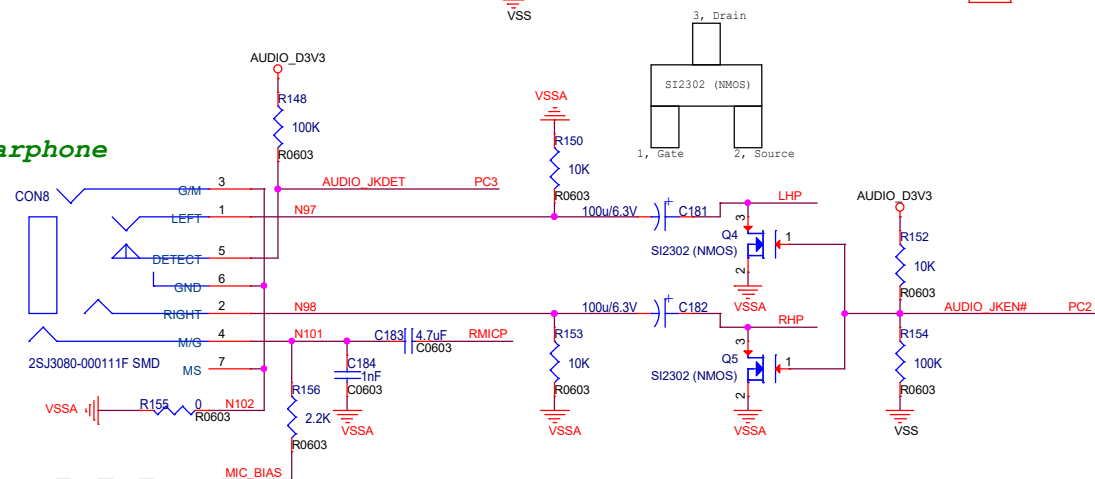
Codec



Speaker



Earphone

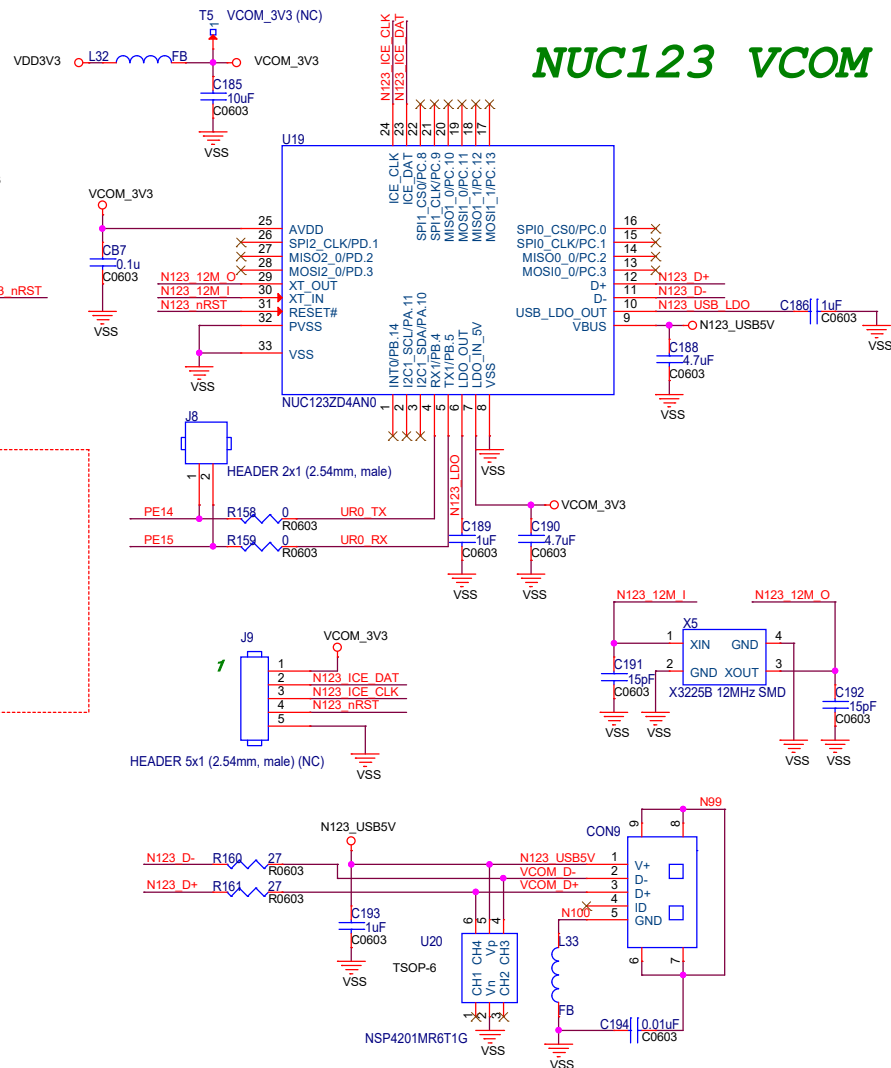


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Note: The GPIO PE15 (UART0_RXD) pin must be pulled to high level through an external resistor or an internal pull-up resistor in the external device (such as a transceiver or MCU).

